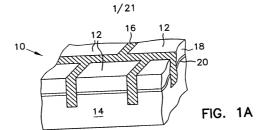
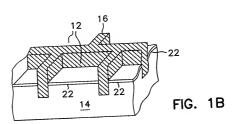
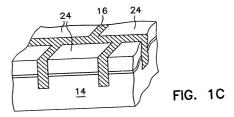
TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.

Dkt #: 303.257US4

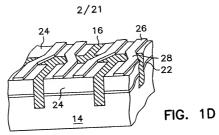






TITLE: METHOD FOR FORWING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.

Dkt #: 303.257US4



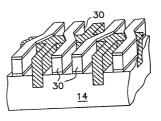


FIG. 1E

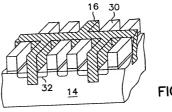


FIG. 1F

TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.
Dkt #: 303.257US4

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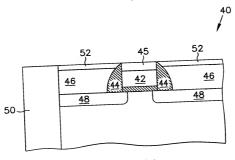


FIG. 2A

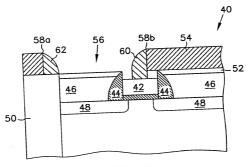
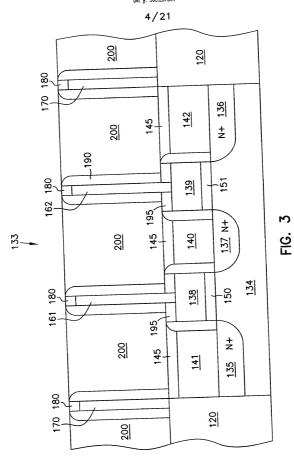


FIG. 2B

TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS, NAME: Wendell P. Noble Jr.

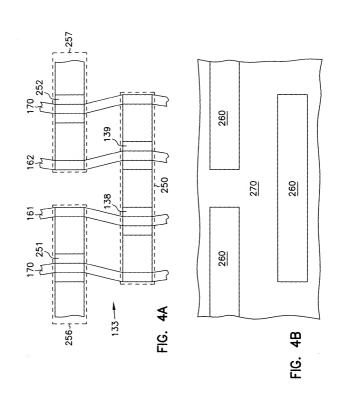
Dix #: 303.257US4



TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendeli P. Noble Jr.

Okt #: 303.257US4

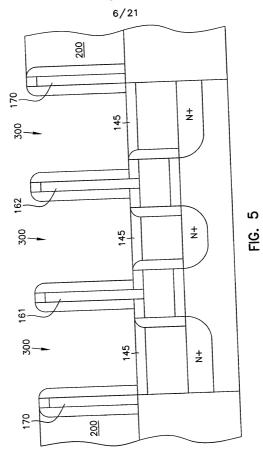
5/21



TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.

Dkt #: 303.257US4





TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTIORS NAME: Wendell P. Nobie Jr.
DIX #: 303.257US4

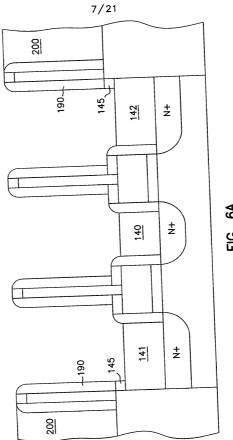
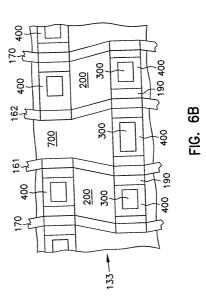


FIG. 6A

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TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.

Dkt #: 303.257US4

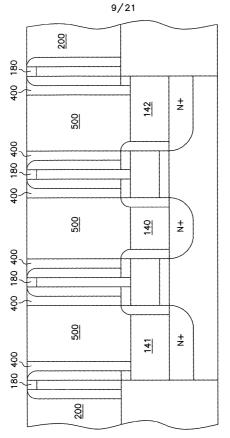
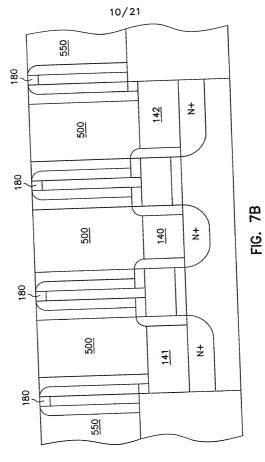


FIG. 7A

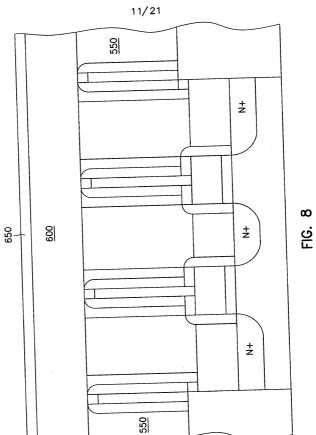
TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME; Wendell P. Noble Jr.
Dkt #: 303.257US4





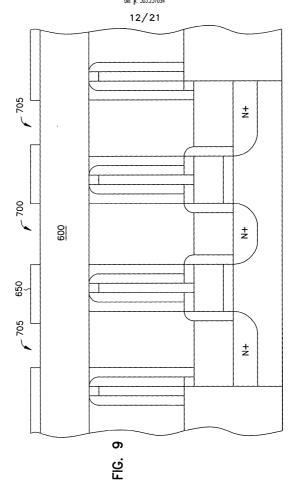
TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.

Dkt #: 303.257US4



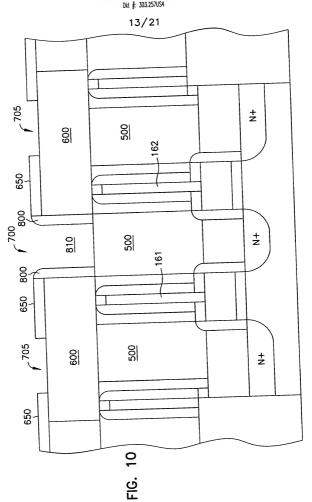
TITLE: METHOO FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.

Okt #: 303.257US4



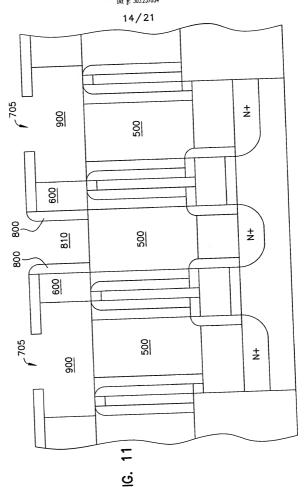
TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.

Dkt #: 303.257US4



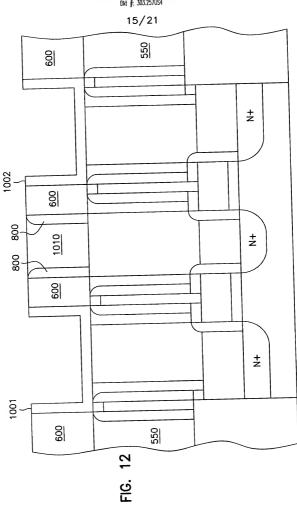
TITLE: METHOD FOR FORMING CATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.

Dial #: 303.257US4



TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.

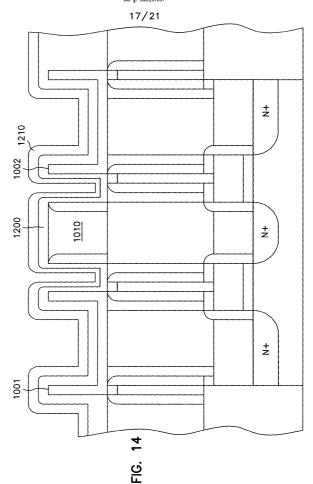
DIA #: 303.257US4



TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wiendell P. Noble Jr.
Dkt #: 303.257US4 16/21 220 ŧ 1010 ŧ ŧ 550

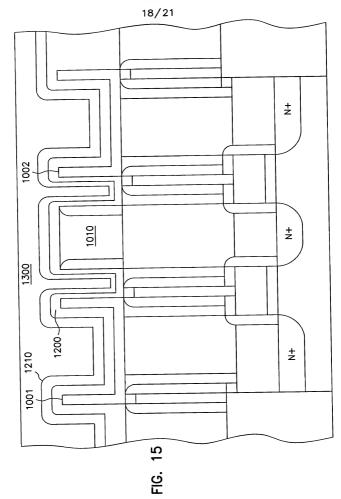
TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.

Dixt #: 303.257US4



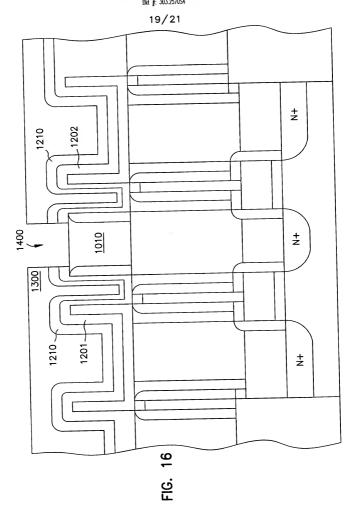
TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.

Dkt #: 303.257US4



TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT NIVENTORS NAME: Wendell P. Noble Jr.

Dkt #: 303.257US4



TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr.

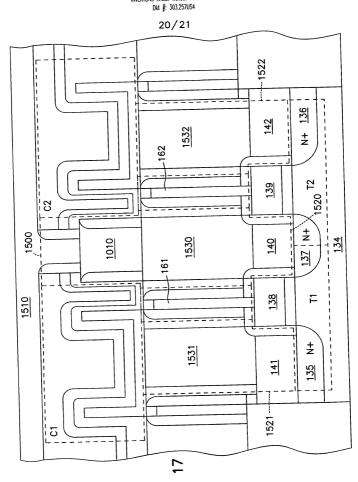


FIG.

TITLE: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT INVENTORS NAME: Wendell P. Noble Jr. Dkt #: 303.257US4



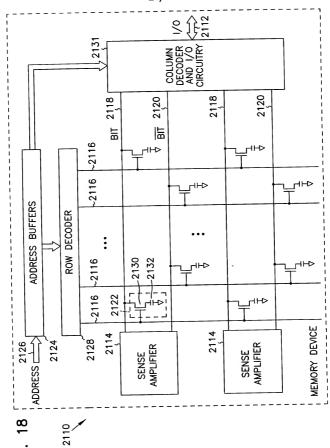


FIG. 1